

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Brian S. Doyle, et al.

Application No.: 09/229,592

Filed: January 13, 1999

For: A TRANSISTOR WITH REDUCED SERIES
RESISTANCE JUNCTION REGIONS

Assistant Commissioner for Patents
Washington, D.C. 20231



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on March 29, 1999
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Cindy Baglietto
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Cindy Baglietto 3/29/99
Signature Date

REQUEST FOR CORRECTION OF FILING RECEIPT

Sir:

On February 4, 1999, we received a corrected official filing receipt in connection with the above-identified patent application (copy enclosed).

However, the following information needs to be corrected due to typographical error occurring in the inventors' names. Please change:

FROM:

Inventor(s): BRIAN S. DOULE, CUPERTINO, CA; DAVID B. FRASE, DANVILLE, CA.

TO:

Inventor(s): BRIAN S. DOYLE, CUPERTINO, CA; DAVID B. FRASER, DANVILLE, CA.

Please correct your records to reflect the above information and then forward to us a corrected filing receipt.

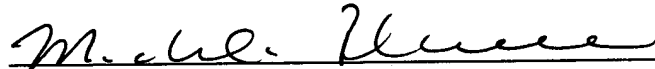
If you have any questions please contact the undersigned.

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Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 3/23, 1999



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STATUS DB-LA

APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTORNEY DOCKET NO.	DRWGS	TOT CHGND CL
09/229,592	01/13/99	2811	\$850.00	42390.P5578	7	25

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LOS ANGELES

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Application Processing Division's Customer Correction Branch within 10 days of receipt. Please provide a copy of the Filing Receipt with the changes noted thereon.

Applicant(s)

BRIAN S. DOULE, CUPERTINO, CA; DAVID B. FRASE, DANVILLE, CA.
Doyle *Fraser*

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TITLE

TRANSISTOR WITH REDUCED SERIES RESISTANCE JUNCTION REGIONS

PRELIMINARY CLASS: 257



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DATA ENTRY BY: ZIMMERMAN, SHUANA

TEAM: 01 DATE: 01/29/99